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Remarks/Arguments

Claims 1-17 are pending in this application and are rejected in the final Office Action of August 3, 2007. Claims 1, 7, 9, 12, 13 and 14 are amended herein to more particularly point out and distinctly claim the subject matter Applicants regard as the invention.

Rejection of Claims 1-9 and 11-17 under 35 U.S.C. §103(a)

Claims 1-9 and 11-17 are rejected under 35 U.S.C. §103(a) as being unpatentable over U.S. Patent No. 6,421,069 issued to Ludtke et al. (hereinafter, "Ludtke") in view of U.S. Patent No. 6,370,322 issued to Horiguchi et al. (hereinafter, "Horiguchi"), and further in view of U.S. Patent No. 5,477,262 issued to Banker (hereinafter, "Banker"). Applicants respectfully traverse this rejection for at least the following reasons.

Applicants first note that independent claim 1, as amended herein, recites:

> "(b) means for providing digital video content;
> (c) means for generating, in said peripheral consumer electronic device, digital OSD video data representative of an on-screen display menu associated with said peripheral consumer electronic device; and
> (d) means for transferring said digital video content and said digital OSD video data as separate data via said digital bus to said display device, wherein at said display device said digital video content passes through a first signal path which decodes said digital video content to generate decoded digital video content and said digital OSD video data passes through a second signal path which does not decode said digital OSD video data, and wherein outputs of said first and second signal paths are combined so that said on-screen display menu represented by said digital OSD video data is overlaid onto said decoded digital video content." (emphasis added)

As indicated above, amended independent claim 1 defines a peripheral consumer electronic device that provides digital video content and generates digital OSD video data representative of an on-screen display menu associated with the peripheral consumer electronic device. The digital video content and digital OSD video data is transferred as separate data to a display device via a

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digital bus. At the display device, the digital video content passes through a first signal path which decodes the digital video content to generate decoded digital video content, and the digital OSD video data passes through a second signal path which does not decode the digital OSD video data. Outputs of the first and second signal paths are combined so that the on-screen display menu represented by the digital OSD video data is overlaid onto the decoded digital video content. Independent claims 7, 9 and 12-14 are amended herein to define the foregoing subject matter in a similar manner. Support for the foregoing claimed subject matter is shown, for example, in FIG. 4 of Applicants' specification.

None of the cited references, whether taken individually or in combination, teach or suggest the foregoing claimed subject matter. In particular, Ludtke discloses a system wherein various peripheral devices connected to a network provide self-describing information that is used to generate a graphical user interface through which the user can control the operation of the devices. In particular, the graphical user interface displays icons that represent the various devices available within the network and the actual topology of the connections (See Fig. 5, column 9, lines 14-17). However, Ludtke provides little detail regarding how digital video content and the digital OSD video data are processed in a display device, and thereby fails to teach or suggest the foregoing claimed subject matter.

Neither Horiguchi nor Banker is able to remedy these deficiencies of Ludtke. In particular, Horiguchi is cited for allegedly teaching the use of isochronous and asynchronous transfer mechanisms for transferring video content and data (see page 3 of the final Office Action dated August 3, 2007). However, as shown for example in FIGS. 2A-2B and its accompanying description, Horiguchi fails to teach or suggest, *inter alia*, the claimed manner of processing digital video content and the digital OSD video data in a display device (e.g., the digital video content passes through a first signal path which decodes the digital video content to generate decoded digital video content, and the digital OSD video data passes through a second signal path which does not decode the digital OSD video data). Rather, as shown in FIG 2B of Horiguchi, the data received via 1394 asynchronous

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section 51A and the data received via 1394 isochronous section 51B are both decoded prior to output.   Accordingly, Horiguchi is unable to remedy the deficiencies of Ludtke.

Banker is likewise unable to remedy the deficiencies of Ludtke and Horiguchi. In particular, Banker is cited for allegedly teaching the use of an overlay function in which menu characters are overlaid onto a video image (see page 4 of the final Office Action dated August 3, 2007). However, as shown for example in FIG. 3 and its accompanying description, Banker fails to teach or suggest, *inter alia*, the claimed manner of processing digital video content and the digital OSD video data in a display device (e.g., the digital video content passes through a first signal path which decodes the digital video content to generate decoded digital video content, and the digital OSD video data passes through a second signal path which does not decode the digital OSD video data). Accordingly, Banker is unable to remedy the deficiencies of Ludtke and Horiguchi.

Therefore, for the reasons stated above, Applicants submit that neither Ludtke, Horiguchi nor Banker, whether taken individually or in combination, teaches or suggests a notable feature of independent claims 1, 7, 9 and 12-14, and their respective dependent claims. Accordingly, withdrawal of the rejection is respectfully requested.

Rejection of Claim 10 under 35 U.S.C. §103(a)

Claim 10 has been rejected under 35 U.S.C. §103(a) as being unpatentable over Ludtke in view of Horiguchi and Banker, and further in view of P1394 Draft 8.0v2. Applicants respectfully traverse this rejection since the P1394 Draft 8.0v2 is unable to remedy the deficiencies of Ludtke, Horiguchi and Banker pointed out above with reference to claims 1-9 and 11-17. In particular, P1394 Draft 8.0v2 is cited for allegedly disclosing a function control protocol in which a peripheral device transmits a control command and response by asynchronous packet for each asynchronous operation (see page 16 of the final Office Action dated August 3, 2007). However, like Ludtke, Horiguchi and Banker, P1394 Draft 8.0v2 also fails to teach or suggest, *inter alia*, the claimed manner of processing digital video

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content and the digital OSD video data in a display device (e.g., the digital video content passes through a first signal path which decodes the digital video content to generate decoded digital video content, and the digital OSD video data passes through a second signal path which does not decode the digital OSD video data). Accordingly, claim 10 is patentably distinguishable over the combination of Ludtke, Horiguchi, Banker and P1394 Draft 8.0v2, and withdrawal of the rejection is respectfully requested.

Conclusion

Having fully addressed the Examiner's rejections it is believed that, in view of the preceding amendments and remarks/arguments, this application stands in condition for allowance. Accordingly then, reconsideration and allowance are respectfully solicited. If, however, the Examiner is of the opinion that such action cannot be taken, the Examiner is invited to contact the applicant's attorney at (609) 734-6815, so that a mutually convenient date and time for a telephonic interview may be scheduled.

Respectfully submitted,

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